

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Donald S. Gardner	Examiner:	Unknown
Serial No.:	Unknown	Group Art Unit:	Unknown
Filed:	Herewith	Docket:	884.140US3
Title:	INDUCTORS FOR INTEGRATED CIRCUITS, INTEGRATED CIRCUIT COMPONENTS, AND INTEGRATED CIRCUIT PACKAGES		
Assignee:	Intel Corporation	Customer No:	21186

INFORMATION DISCLOSURE STATEMENT

MS Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

Pursuant to 37 C.F.R. §1.98(d), copies of the listed documents are not provided as these references were previously cited by or submitted to the U.S. Patent Office in connection with Applicant's prior U.S. applications, Serial No. 10/238,539, filed on September 10, 2002, and/or Serial No. 09/444,608, filed on September 23, 1999, now issued as U.S. Patent No. 6,452,247, which are relied upon for an earlier filing date under 35 U.S.C. §120.

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The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

DONALD S. GARDNER

By his Representatives,

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Date Feb. 6, 2004

By Ann M. McCrackin

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Date of Deposit: February 9, 2004

This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to the Commissioner for Patents, Mail Stop Patent Application, P.O. Box 1450, Alexandria, VA 22313-1450.

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number Unknown**Filing Date** Even Date Herewith**First Named Inventor** Gardner, Donald**Group Art Unit** Unknown**Examiner Name** Unknown

Sheet 1 of 1

Attorney Docket No: 884.140US3

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
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	US-6,344,125	02/05/2002	Locke, P. S., et al.	205	118	04/06/2000

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Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
	EP-0725407	08/07/1996	Nmh Hsieh, Chang-Ming, et al.	H01F	17/00	
	JP-07272932	10/20/1995	Terayama, Yoshimi	H01F	17/00	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		SHIRAKAWA, K., et al., "Thin film cloth-structured inductor for magnetic integrated circuit", <u>IEEE Transactions on Magnetics</u> , Vol. 26, No. 5, XP000150520, (Sept. 1990),2262-2264	
		YAMAGUCHI, M., et al., "Characteristics of magnetic thin-film inductors at large magnetic field", <u>Proceedings of the Int'l Magnetics Conf.</u> , XP000581941, (April 18, 1995),BS-1	

EXAMINER

DATE CONSIDERED